

A HYBRID ALGORITHM DESIGN FOR XOR ENCRYPTION TECHNIQUES WITH A COMBINATION OF CBC - ECB 512-BITS BLOCK CIPHER MODES AND RSA ALGORITHMS

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ABSTRACT

This research was conducted by analyzing the combination of RSA algorithm with ECB and CBC block cipher modes with a 512-bit block length. The aim of this research is to obtain a cryptography algorithm that is fast, resource-efficient, and free from key distribution problems. The data used in the testing process was text data with message lengths ranging from 10 characters to 1 million characters. The results of this research indicate that the combination of RSA algorithm with CBC-ECB 512-bit block cipher mode is able to provide optimal security for the data. The resulting cipher text and cipher key will be very secure from exhaustive search or brute force attacks. It would take up to 1.80×10^{257} years to decrypt the cipher text, while it would take 3.17×10^{166} years to break the cipher key using exhaustive search or brute force techniques.

Keywords—RSA, Encryption, Cipher

1. INTRODUCTION.

Cryptography WILL keep information secret by encoding it into a form that the meaning can no longer be understood. Currently, many cryptographic algorithms have emerged which are continuously analyzed, tried, and refined to find algorithms that are considered to meet security standards. Each algorithm has a different level of security, as well as a different level of complexity. An algorithm with a high level of security and low complexity would be very good to implement, this is because the encryption and decryption process is much faster and consumes less resources. However, every cryptographic algorithm that has been found has its own weaknesses.

One of the fastest cryptographic algorithms is the cryptographic algorithm with the XOR technique. Even though it is very simple, the XOR technique is the choice for fast encryption process needs. The XOR technique will be very safe if it is set in the right process scheme, if the process scheme is not right, then the XOR technique will be very unsafe [1].

The XOR technique is included in the symmetric cryptographic algorithm section, where the key for the encryption and decryption processes is only the same key. Because in the process of encryption and decryption the XOR technique performs a one-to-one correspondence process, or one key bit will be XORed against one bit of plain text, the encryption and decryption process in the XOR technique is applied to the block cipher operating mode to increase the security of cipher text produced [2].

In block cipher operation, there are several modes that can be used, some of which are ECB (Electronic Code Book) and CBC (Cipher Block Chaining) operating modes. ECB and CBC are forms of operation of the block cipher mode in the symmetric algorithm. In the block cipher scheme, the plain text will first be divided into blocks of n-bits long and then each block will be operated on. The type of operation that can be used for this mode of operation is the XOR operation technique. The advantages of the XOR technique are its speed and simplicity in the process, but still promises high security for the resulting cipher text

[3].



The strength or security of the block cipher mode of operation lies mainly in the length of the block used, the longer the block used, the more secure the cipher text is. In this study, the proposed block length is 512-bits long. With a length of 512-bits, this algorithm has 2512 possible keys and possible variations of cipher text or around 1.34 x 10154 different key combinations [4].

This research tries to combine the block cipher Electronic Code Book (ECB) and Cipher Block Chaining (CBC) algorithms into one complete mode of operation to produce a super ciphertext that is much safer and stronger than the previous cipher text, supported by two secret keys. which have a key length of 512 bits each. With this combination, it will generate possible key combinations as long as 21024 which will increase the security of the lock without sacrificing the length of the processing block. As with other types of symmetric cryptographic algorithms, the main drawback of the XOR technique is the distribution key problem. where the sender of the message will have great difficulty in safely distributing the key to the recipient of the message so that the message can be decrypted back into the original message (plain text). If the key falls into the hands of eavesdroppers, the encryption process that has been carried out will be in vain [2].

To overcome the key distribution problem that occurs, an asymmetric algorithm is used to encrypt the generated key. The asymmetric algorithm used is the RSA algorithm designed by Ron Rivest, Adi Shamir, and Leonard Adleman, in the 1970s [5].

II. LITERATURE REVIEWS.

2.1. ECB (ELECTRONIC CODE BOOK)

Is one of the processing modes in the block cipher. ECB mode is suitable for encrypting randomly accessed files because each block of plain text is encrypted independently. Even if ECB mode is done with parallel processors, each processor can encrypt or decrypt different plain text blocks [3].

In the Electronic Code Book (ECB) operating mode, encryption is performed by XORing each block of plain text with a key that has the same bit length as each block, as shown in the following figure:

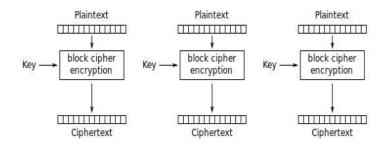


Figure 1.ECB Operation Mode Process Schematic

2.2. CBC (CIPHER BLOCK CHAINING)

The Cipher Block Chaining (CBC) mode implements a feedback mechanism on a block, in which case the results of the previous block encryption are fed back into the encryption block being processed. The trick is that the plain text block that is being processed is XORed first with the cipher text block previously encrypted, then the results of this XOR are entered into the encryption function [3].

In Cipher Block Chaining (CBC), encryption is done by XORing the first block of plain text with an IV (Initialization Vector) which consists of 0 bits along n-bits, then XORing it again with the key to

2	International Journal in IT and Engineering
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generate cipher text for the first block. This cipher text is used as the IV (Initialization Vector) for the next block encryption, and so on. As shown in the following figure:

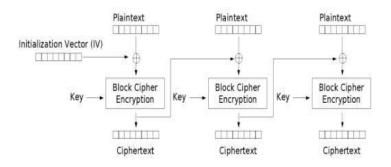


Figure 2.CBC Operation Mode Process Schematic

2.3. RSA

RSA is an asymmetric cryptographic algorithm having two keys, namely a public key and a private key designed by Ron Rivest, Adi Shamir, and Leonard Adleman. The strength of RSA is highly dependent on the complexity of factoring integers into two different prime numbers. RSA only uses exponential operations for encryption and decryption operations. RSA is an asymmetry algorithm [6].

The RSA algorithm consists of 3 processes, namely:[7]

- 1. Key Generator
 - 1) Choose two random prime numbers, p and q.
 - 2) Calculate the system modulus

$$n = p * q$$

3) Search Totient
$$\Phi(n)$$

$$\Phi(n) = (p-1)(q-1)$$

4) Choose encryption key e randomly

Where $1 < e < \Phi(n)$, gcd (e, $\Phi(n)$) = 1

5) Determine the decryption key d with the following equation:

$$d \equiv e - 1 \pmod{\Phi(n)}$$

Where the above equation is equivalent to:

 $e * d \equiv 1 \pmod{\Phi(n)}$, where $0 \le d \le n$

Key generation result:

a. *Private keys*=(d,n)

is highly confidential, and only the recipient of the message may know it.

b. *Public keys* = (e,n)

It is not secret, and may be distributed freely.

2. Encryption

In general, the encryption process with RSA is done with the following formula:

- Ci = Piemod n
- 3. Description

In general, the decryption process with RSA is carried out using the following formula: $Pi = Cid \mod n$



III. RESEARCH METHODS

The encryption scheme of the proposed hybrid algorithm can be seen in the following figure:

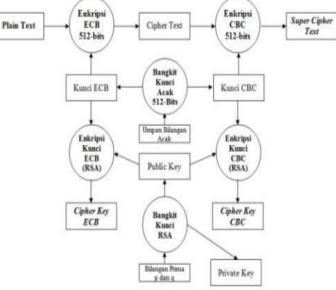
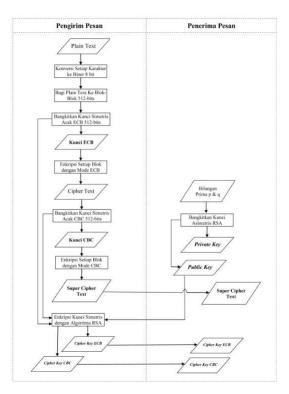
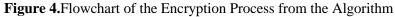


Figure 3. Hybrid Algorithm Encryption Scheme

The decryption process in the proposed Hybrid algorithm can be seen in the following flowchart:





Hybrid



The decryption scheme of the proposed hybrid algorithm can be seen in the following figure:

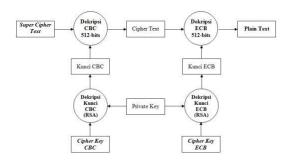


Figure 5.Schematic Decryption of the Hybrid Algorithm

The decryption process in the proposed Hybrid algorithm can be seen in the following flowchart:

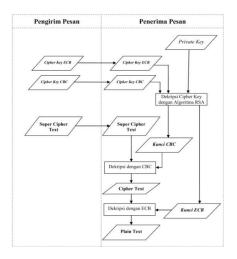


Figure 6.Flowchart of the Decryption Process of the Hybrid Algorithm

The process workflow in the proposed hybrid algorithm scheme follows the following flow:

- 1. First Phase (RSA Key Generation)
- 2. Second Stage (Encryption) a. ECB Key Generation
 - b. CBC Key Generation
 - c. Encryption Process with ECB Mode
 - d. Encryption Process with CBC Mode
 - e. ECB Key Encryption Process with RSA Algorithm
 - f. CBC Key Encryption Process with RSA Algorithm
- 3. Third Stage (Decryption)
 - a. CBC Cipher key decryption
 - b. ECB Cipher key decryption



- c. Super cipher text decryption with CBC Operation Mode
- d. Cipher text decryption with ECB Operation Mode

VI. RESULTS AND DISCUSSION.

Testing is done against the schema

proposed hybrid algorithm. In this discussion, there is a plain text that will be tested, while the plaintext is:

Plain text : Medan City, Capital of North Sumatra Province, Medan City is the largest city outside Java in Indonesia.

Generate the RSA key to get a public key (3149, 2159869) and a private key (1010249, 2159869). Then generate ECB and CBC symmetric keys randomly 512-bits long. ECB 512-bits random key used as follows:

000101010110010111000111101110011000

011000100010001010100111001100011011001

010110010101100111010000010001111011111

011111110001101111100000101100110001011

0101011110010100100111001001011111110001

111000101010010111100111000000111101000

111111000110011010101011010101010101000

1111101100101011010110011110011100011

011100010000000001000001010011010001

01011110010000011110001110010110000100

00101101

CBC 512-bits random key used as follows: 00111010000111010100001101110010001



000001000011100011101111011001101101000

011101000101101100100001000010111111011

100111001111101010111101111001111100001

011011000111111010110010100010011001101

111010001001010111100111011001100000010

01110011010011101011010000010101000010

00101111101100000010000101010000011010

111001010001010110001111010111010101110

010000110110110101100001110000011011110

0111101101011100001111100101111011000100

01000101

The length of the plain text to be encrypted is 106 characters or 848 bits. Convert plain text in binary form and then divide it into blocks of 512-bits length. If the last block is less than 512-bits long, then add the padding bit '1' until the last block is 512-bits long. The process of dividing plain text into 512-bit blocks is as follows:

Plain Block 0



Plain Block 1

011011100010000010010110110111101110100011000

Perform XOR operations with 512-bits ECB mode as shown in the following table:

Plain text Block		
to - 0	ECB lock	Encryption Results
010010110110	000101010110	010111100000
11110	01011	10101
111010001100 00100	100011110111 00110	011001111011 00010
100000010011	000101011010	100101001001
01011	11000	10011
001010110010 00110		101101001111 01011
000101101110	101001101100	101100000010
00100	01000	01100
000010010010 11000	100010101001 11001	100000111011 00001
100111010100	100011011001	000100001101

Table 1.0th Block Plain text Encryption with ECB Key

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10000	01011	11011
001001011011	001010110011	000011101000
01111	10100	11011
011101000110	000100011110	011001011000
00010	11111	11101
010000001010	011111110001	001111111011
00001	10111	10110
110010011011	110000010110	000010001101
11011	01100	10111
101100110100	010111010101	111011100001
10110	10001	00111
111001110011	110000100111	001001010100
01101	11100	10001
001001000000	011000111001	010001111001
10100	11110	01010
110111010101	011001010011	101110000110
10110	00011	10101
101100001011	001011011001	100111010010
10100	10001	00101
011001010111	010111100101	001110110010
00100	00100	00000
110000100100	111001001011	001001101111
00001	11110	11111
010101011101	001111000101	011010011000
00011	01001	01010
000010111001	011110011100	011100100101
00110	00001	00111
000100101100	111010001111	111110100011
00100	11000	11100
000010010110	110011010100	110001000010
11011	10110	01101
110111010001	101011010100	011100000101
10000	01111	11111
100100000010	101100101011	001000101001
01101	01011	00110



011001010110 01000		010110011000 11001
110000101101 11000	101110001000 00000	011110100101 11000
100000010011 01011	000100000010 10011	100100010001 11000
001010111001 00111		011011101110 10101
010101110000 01100		010100001000 10000
001011010110 11000		100111011110 10011
01	01	00

Table 2.1st Block Plain text Encryption with ECB Key

<i>plain</i> <i>text</i> block to	ECB lock	Encryption Results
-1		
000000	001010101011	810110100
10010110110	10001111011	000110011010
111101	100110	11011
11010001100	00010101101 011000	110001000010 10001
00000010101	1001111110	100111010111
000110	101101	01011
01010111001	10100110110	111100011110
001100	001000	00100
011100	10001010100	110001100011
011100	111001	00101
11011000010	10001101100	010101011100
111001	101011	10010
00010000001	00101011001	001110110000
100100	110100	10000
01101001001	00010001111	011110001100
000000	011111	11111
10011000111	0111111000	111001111111
010101	110111	00010
10000101110	11000001011	010001001010
010001	001100	11101



00000010010	01011101010	010111110000
100110	110001	10111
00010111011	11000010011	110101010000
101100	111100	10000
00100100000	01100011100	010001111001
011001	111110	00111
00011010010	01100101001	011111110111
010000	100011	10011
00100100101	00101101100	000010010010
101110	110001	11111
01100100011	01011110010	001110100011
011110	100100	11010
11011100110	11100100101	001110000111
010101	111110	01011
11001101101	00111100010	111100011111
001011	101001	00010
00001001011	01111001110	011100001011
101111	000001	01110
111111111111	11101000111	000101110000
111111	111000	00111
	11001101010 010110	001100101011 01001
111111111111	10101101010	010100101011
111111	001111	10000
	10110010101 101011	010011010100 10100
111111111111	00111100111	110000110001
111111	010001	01110
	10111000100 000000	010001110111 11111
111111111111	00010000001	111011111101
111111	010011	01100
	01000101011 110010	101110101000 01101
111111111111	00000111100	111110000111
111111	011100	00011
<u>+++++</u>	001011	010011110111 10100
11	01	10

V. CONCLUSION.

By combining block operation modes*cipher*ECB and CBC are 512-bits long with the RSA algorithm, a fast, lightweight and secure cryptographic algorithm will be produced. Where the resulting cipher text and cipher key are very unlikely to be solved by exhaustive search. The cipher text and cipher key generated from the proposed algorithm are very safe from exhaustive search or gross force attacks. Where the time needed to solve the resulting cipher text takes 1.80 x 10^{257} Years, while for the cipher key it takes 3.17 x 10166 years. with the RSA algorithm, a cryptographic algorithm that is fast, lightweight, and secure will be generated. Where the resulting cipher text and

11	International Journal in IT and Engineering
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cipher key are very unlikely to be solved by exhaustive search. The cipher text and cipher key generated from the proposed algorithm are very safe from exhaustive search or gross force attacks. Where the time needed to solve the resulting cipher text takes 1.80 x 10257 years, while for the cipher key it takes 3.17 x 10166 years

REFERENCE

- Aryza, S., Irwanto, M., Khairunizam, W., Lubis, Z., Putri, M., Ramadhan, A., Hulu, F. N., Wibowo, P., Novalianda, S., & Rahim, R. (2018). An effect sensitivity harmonics of rotor induction motors based on fuzzy logic. *International Journal of Engineering and Technology(UAE)*, 7(2.13 Special Issue 13), 418–420. https://doi.org/10.14419/ijet.v7i2.13.16936
- [2] Wairya, S., Kumar. R., Nagaria., & Tiwari, (2012). Comparative Performance Analysis of XORXNOR Function Based High-Speed CMOS Full Adder Circuits For Low Voltage VLSI Design. International Journal of VLSI design & Communication Systems (VLSICS) Vol.3, No.2, April 2012
- [3] Kumar, S., Suneetha, CH, & Chandrasekhar, A. (2011). A Block Cipher Using Rotation and Logical XOR Operations. IJCSI International Journal of Computer Science Issues, Vol. 8, Issue 6, No 1, November 2011 ISSN (Online): 1694-0814
- [4] Dashti, A., Kheradmand, HA, & Jazi, M. (2016). Comparison Of Three Modes Of Cryptography Operation For Providing Security and Privacy Based on Important *factors*. International Journal of Information Technology and Electrical Engineering Volume 5, Issue 3 ISSN: - 2306-708 X June 2016
- [4] Sridevi. (2014). Construction of Stream Ciphers from Block Ciphers and their Security. IJCSMC, Vol. 3, Issues. 9, September 2014, pg. 703 – 714
- [5] Munir, R. (2006). Cryptography. Informatics: Bandung.
- [6] Kallam, RB (2011. An Enhanced RSA Public key Cryptographic Algorithm. International Journal of Advanced Research in Computer Science (IJARCS)
- [7] Singh, S. (2013). A Performance Analysis of DES and RSA Cryptography. International Journal of Emerging Trends & Technology in Computer Science (IJETTCS)